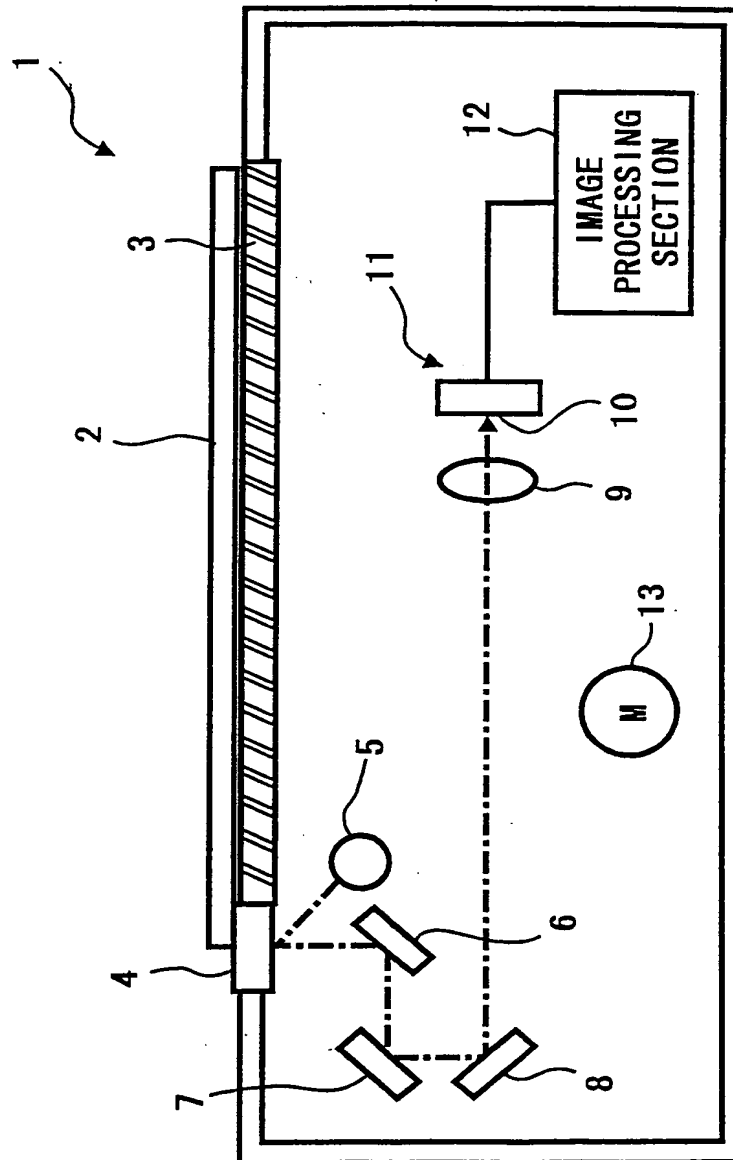


FIG. 1



ANALOG SIGNAL PROCESSING CIRCUIT 21

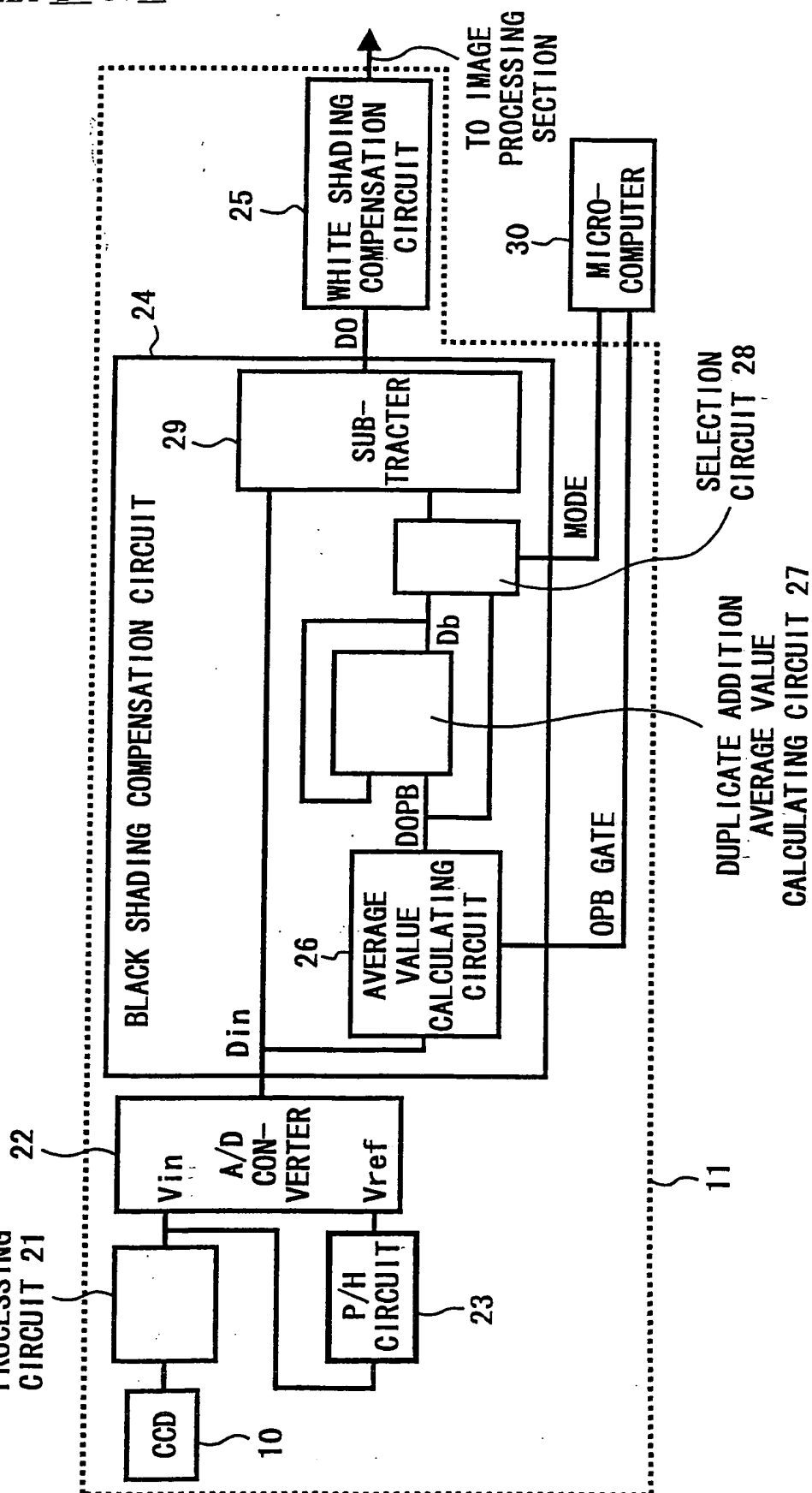


FIG. 3

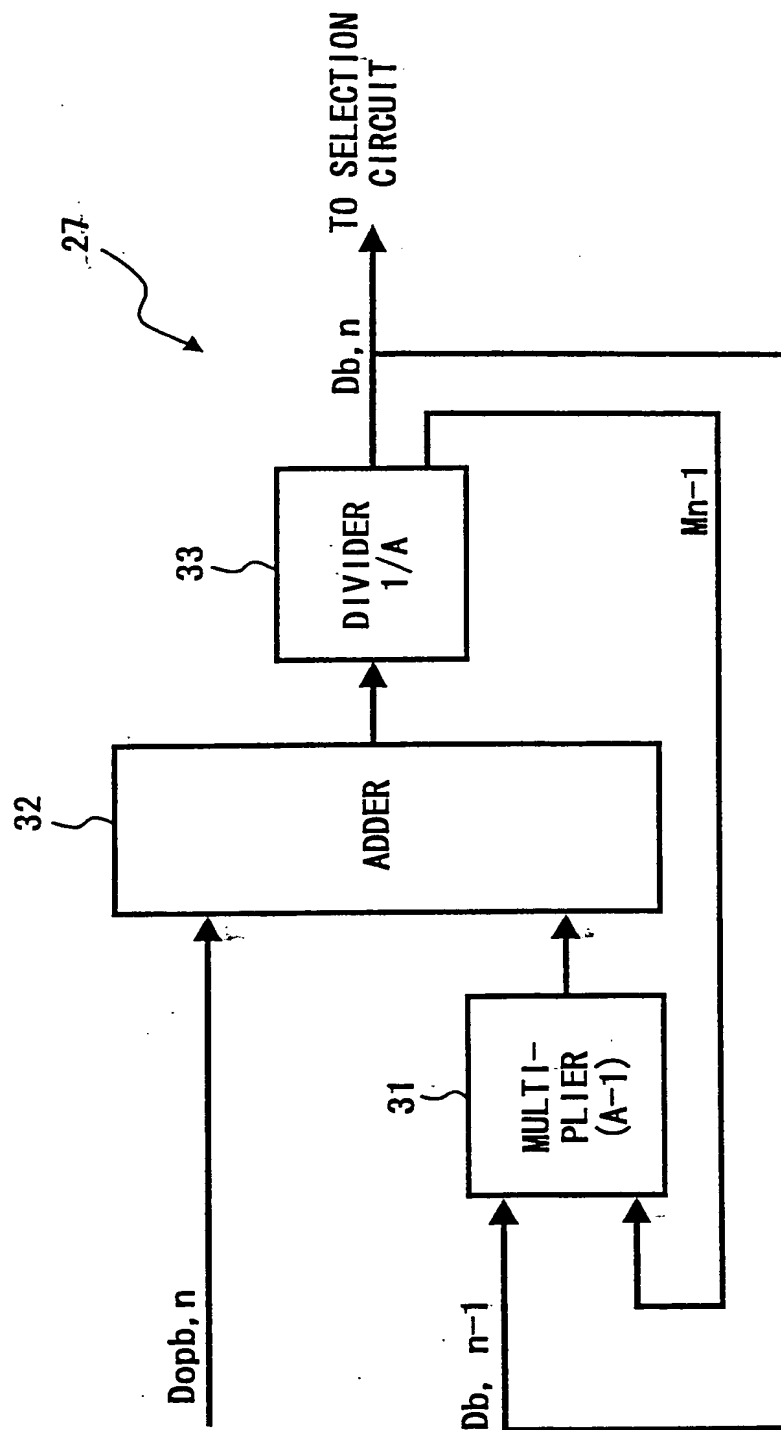


FIG. 4

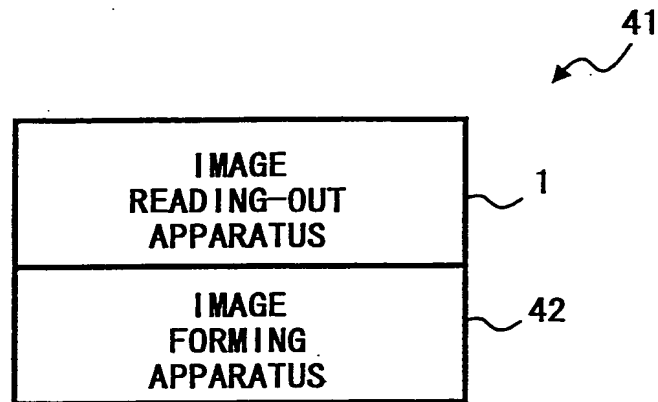


FIG. 5

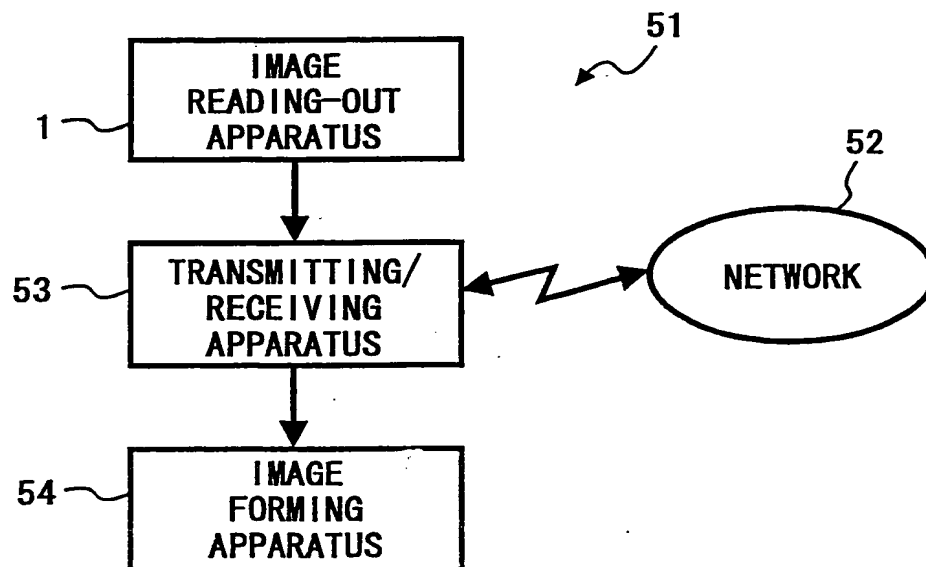


FIG. 6

ANALOG AMOUNT			DIGITAL AMOUNT					
Vref	Vin	Voph	Din	Doph	Db	M	Do	AT THE TIME OF Db
								AT THE TIME OF Dopb
1.65	1.5	0.1	232	15	15	0.455	217	217
1.65	1.5	0.1	232	15	15	0.341	217	217
1.65	1.5	0.1	232	15	15	0.256	217	217
1.65	1.5	0.1	232	15	15	0.192	217	217
1.65	1.5	0.1	232	15	15	0.144	217	217
1.98	1.8	0.1	232	13	15	-0.392	219	217
1.98	1.8	0.1	232	13	14	0.206	219	218
1.98	1.8	0.1	232	13	14	-0.096	219	218
1.98	1.8	0.1	232	13	14	-0.322	219	218
1.98	1.8	0.1	232	13	14	-0.491	219	218
1.98	1.8	0.1	232	13	13	0.382	219	219
1.98	1.8	0.1	232	13	13	0.286	219	219
1.98	1.8	0.1	232	13	13	0.215	219	219
1.98	1.8	0.1	232	13	13	0.161	219	219
1.98	1.8	0.1	232	13	13	0.121	219	219
1.98	1.8	0.1	232	13	13	0.091	219	219
1.98	1.8	0.1	232	13	13	0.068	219	219
1.98	1.8	0.1	232	13	13	0.051	219	219
1.98	1.8	0.1	232	13	13	0.038	219	219
1.98	1.8	0.1	232	13	13	0.029	219	219